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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Roy E. Scheuerlein, et al.

Title: MULTIPLE TWIN CELL NON-VOLATILE MEMORY ARRAY AND  
LOGIC BLOCK STRUCTURE AND METHOD THEREFOR

Application No.: 10/675,212 Filed: September 30, 2003

Examiner: Ho, Hoai V. Group Art Unit: 2818

Atty. Docket No.: 023-0024 Confirmation No.: 9946

March 16, 2005

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**INFORMATION DISCLOSURE STATEMENT**  
**37 C.F.R. § 1.97(c)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings to the Examiner's attention in the above-identified application the patents, publications, applications or other information identified in the attached:

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- ☐ Other: N/A

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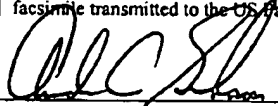
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The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of (i) a final action under § 1.113, (ii) a notice of allowance under § 1.311 or (iii) an action that otherwise closes prosecution. Accordingly, this Information Disclosure Statement is accompanied by the fee set forth in § 1.17(p) as follows:

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Respectfully submitted,



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